

<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. <b>5035-202</b>		APPLICATION NO. <b>10/518.974</b>		
				APPLICANT(S) <b>Richard Michael Taylor</b>				
				FILING DATE <b>Dec. 21, 2004</b>		GROUP ART UNIT		
<b>U.S. PATENT DOCUMENTS</b>								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<b>U.S. PATENT APPLICATION PUBLICATIONS</b>								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
DJH	A	WO 03/040916 A1	15 May 2003	PCT				
DJH	B	EP 081 0519 A2	30 May 1997	EP				
DJH	C	EP 1 050 811 A1	03 May 1999					
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
DJH		D	Hideki Ando, Chikako Nakanishi, Tetsuya Hara, Masao Nakaya; "Unconstrained Speculative Execution with Predicated State Buffering"; System LSI Laboratory; Mitsubishi Electric Corporation; 22nd ISCA, pp 126-137, June 1993					
DJH		E	Dionisios N. Pnevmatikatos; Gurinder S. Sohi; "Guarded Execution and Branch Prediction in Dynamic ILP Processors"; Computer Sciences Department; University of Wisconsin-Madison; pp 120-129					
EXAMINER <b>/David Huisman/</b>				DATE CONSIDERED <b>09/13/2006</b>				
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

  

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
DJH	F	Marc Tremblay; Jeffrey Chan; Shallender Chaudhry; Andrew W. Conigliaro; Shing Sheung Tse; "The MAJC Architecture: A Synthesis of Parallelism and Scalability"; November - December, IEEE Micro; pp 12 - 25	
DJH	G	Jenn-Yuan Tsai; Pen-Chung Yew; "Enhancing Multiple-Path Speculative Execution with Predicate Window Shifting"; pp 0 - 27	

  

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